

SCHEMATIC DIAGRAM
MODEL NO. A 457
SERIAL NO. 409401 & ABOVE.

S C O T T

SCHALTUNGSDIENST LANGE 1000 BERLIN 47 PF. 47 06 53
TEL. 030/603 20 03 TX 184339 BTX #360 310 000 140#

- NOTES:
- 1 RESISTANCE IN OHMS $\pm 10\%$, 1/4W; CAPACITANCE IN MICROFARADS, UNLESS OTHERWISE SPECIFIED.
 - 2 UNLESS OTHERWISE SPECIFIED, ALL DC VOLTAGES $\pm 15\%$ MEASURED WITH RESPECT TO CHASSIS GROUND, USING VTVM, POWER LINE 120VAC
 - 3 DC OFFSET VOLTAGE - WITH NO SIGNAL APPLIED, READ 0 VOLTS $\pm 25\text{mV}$ AT SPEAKER A OUTPUT TERMINAL (25 mV IS 1.25 mA ON TRIPLET 830A (2mA SCALE).
 - 4 BIAS CURRENT OF OUTPUT TRANSISTORS IS MEASURED AS FOLLOWS: SET VOM TO 12mA RANGE. MEASURE CURRENT ACROSS BOTH 0.27 OHM RESISTORS ON THE DRIVER AMPLIFIER BOARD. READ 0.2mA ON TRIPLET 830A ON 12mA SCALE. USE SAME PROCEDURE FOR OTHER CHANNEL. ALLOW 5 MINUTE WARM-UP BEFORE ADJUSTING BIAS. (SEE ALSO TEST PROCEDURE).

